WE CLAIM:

1.   A method for identification of memory address allocation conflicts of a plurality of finite buffers within a defined memory space, comprising the steps of:

5   entering a list of said buffers and corresponding memory address allocations;

scanning said memory allocations from a first memory address to a second memory address within said memory space;

creating a link list of primary memory addresses correlating

10   to the start and end of each of said buffers;

creating an ordered list of said primary memory addresses and corresponding buffers which include said addresses from said primary address list.

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